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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert F. Wallace
Assignee: SanDisk Corporation
Title: Semiconductor Package Using Terminals Formed On a Conductive Layer of a Circuit Board
Serial No.: Unassigned Filing Date: May 24, 2001
Examiner: Unknown Group Art Unit: Unassigned
Docket No.: M-10238-2C US

San Francisco, California
May 24, 2001

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. The examiner is ask to further note that the cited references marked by an * on the enclosed form PTO-1449 were previously cited in the parent application. Copies of all these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL LABEL NO:

EL 751239346 US

Respectfully submitted,

P. Mikhail

Peter Mikhail
Attorney for Applicant
Reg. No. 46,930

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Sheet 1 of 2

U.S. Department of Commerce, Patent and Trademark Office

Atty Docket No.

Serial No.

M-10238-2C US

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Applicant

(Use several sheets if necessary)

Robert. F. Wallace

Filing Date

Group

May ****, 2001

Unassigned

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If
	AA	Des. 368,082	**3/19/96	Goto	D14	114	
	AB	4,222,516	9/16/80	Badet et al.	235	492	
	AC	4,264,917	4/28/81	Ugon	357	74	
	AD	4,463,971	8/7/84	Hoppe et al.	283	83	
	AE	4,532,419	7/30/85	Takeda	235	492	
	AF	4,649,418	3/10/87	Uden	357	80	
	AG	4,754,319	6/28/88	Saito et al.	357	72	
	AH	4,766,480	8/23/88	Hamada	357	74	
	AI	4,931,853	6/5/90	Ohuchi et al.	357	74	
	AJ	4,990,759	2/5/91	Gloton et al.	235	492	
	AK	4,994,659	2/19/91	Yabe et al.	235	492	
	AL	5,036,430	7/30/91	Hills	361	399	
	AM	5,144,747	9/8/92	Eichelberger	29	839	
	AN	5,272,374	12/21/93	Kodai et al.	257	679	
	AO	5,307,240*	4/26/94	McMahon	361	728	

Foreign Patent Documents

							Translat	
		Document	Date	Country	Class	Subclass	Yes	No
	AP	61-15289 (A)	29.6.1984	Japan	G06K19/00		X	
	AQ	EP535436 A2	14.09.92	Germany	G06K 19/00	B42D 15/10	X	
	AR							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

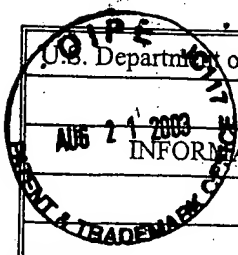
AS	"Smart Card Standards", Smart Card Industry Association, www.scia.org/aboutSmartcards, ISO 7816 Parts 1-3, pp. 1-23 (1/10/00)
AT	

Examiner

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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(Use several sheets if necessary)		Robert F. Wallace	
		Filing Date	Group
		May ***, 2001	Unassigned

U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AU	5,315,241*	5/24/94	Ewers	324	158
	AV	5,375,037	12/20/94	Le Roux	361	684
	AW	5,493,151*	2/20/96	Asada et al.	257	686
	AX	5,550,709	8/27/96	Iwasaki	361	684
	AY	5,612,532	3/18/97	Iwasaki	235	492
	AZ	5,663,901*	9/2/97	Wallace et al.	365	52
	BB	5,581,065	12/3/96	Nishikawa et al.	235	492
	BC	5,677,524	10/14/97	Haghiri-Tehrani	235	492
	BD	5,752,857	5/19/98	Knights	439	638
	BA	5,757,116	5/26/98	Nishikawa et al.	235	492
	BE	5,770,877	6/23/98	Park	257	315
	BF	5,796,164*	8/18/98	McGraw et al.	257	679
	BF	5,822,190	10/13/98	Iwasaki	361	737
	BG	5,906,516	*5/25/99	Sato et al.	439	630
	BH	6,109,939	8/29/00	Kondo et al.	439	140
	BI	6,137,710	10/24/00	Iwasaki et al.	365	52
	BJ	US 6,199,756 B1	3/13/01	Kondo et al.	235	380

Foreign Patent Documents

	Document	Date	Country	Class	Subclass	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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